



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Shunpei Yamazaki Art Unit : 2811
Serial No. : 09/774,388 Examiner : Thien F. Tran
Filed : January 30, 2001
Title : SEMICONDUCTOR DEVICE AND MANUFACTURING METHOD THEREOF

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Copies of the references listed on the attached form PTO-1449 are enclosed.

This filing is being made with the filing of a Request for Continued Examination. No fee is required.

Respectfully submitted,

Date: July 7, 2003



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Substitute Form PTO-1449
(Modified)U.S. Department of Commerce
Patent and Trademark OfficeAttorney's Docket No.
07977-265001Application No.
09/774,388**Information Disclosure Statement
by Applicant**

(Use several sheets if necessary)

Applicant
Shunpei YamazakiFiling Date
January 30, 2001Group Art Unit
2811

(37 CFR §1.98(b))

U.S. Patent Documents

Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	AA	5,206,749	04/27/1993	Zavracky et al.			
	AB	5,317,236	05/31/1994	Zavracky et al.			
	AC	5,376,561	12/27/1994	Vu et al.			
	AD	5,654,811	08/05/1997	Spitzer et al.			
	AE	5,781,164	07/14/1998	Jacobsen et al.			
	AF	6,043,800	03/28/2000	Spitzer et al.			
	AG						
	AH						
	AI						
	AJ						
	AK						

Foreign Patent Documents or Published Foreign Patent Applications

Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
							Yes	No
	AL							
	AM							
	AN							
	AO							
	AP							

Other Documents (include Author, Title, Date, and Place of Publication)

Examiner Initial	Desig. ID	Document
	AQ	
	AR	
	AS	
	AT	

Examiner Signature

Date Considered

EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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